| L Number | Hits | Search Text | DB | Time stamp |
|---|---|---|---|---|
| 1 | 2353 | ((integrated adj circuit) ic asic) near4 (PLD$1 PAL$1 FPLA$1 CPLD$1 EPLD$1 EEPLD$1 LCA$1 FPGA$1) | USPAT | 2004/02/12 14:44 |
| 3 | 28 | (((integrated adj circuit) ic asic) near4 (PLD$1 PAL$1 FPLA$1 CPLD$1 EPLD$1 EEPLD$1 LCA$1 FPGA$1)) same wireless | USPAT | 2004/02/12 14:35 |
| 4 | 136 | ((integrated adj circuit) ic asic) near2 (includ$3 compris$3 ha$2) near2 (PLD$1 PAL$1 FPLA$1 CPLD$1 EPLD$1 EEPLD$1 LCA$1 FPGA$1) | USPAT | 2004/02/12 17:01 |
| 5 | 6 | (((integrated adj circuit) ic asic) near2 (includ$3 compris$3 ha$2) near2 (PLD$1 PAL$1 FPLA$1 CPLD$1 EPLD$1 EEPLD$1 LCA$1 FPGA$1)) same memory same processor | USPAT | 2004/02/12 14:48 |
| 6 | 1 | (((integrated adj circuit) ic asic) near2 (includ$3 compris$3 ha$2) near2 (PLD$1 PAL$1 FPLA$1 CPLD$1 EPLD$1 EEPLD$1 LCA$1 FPGA$1)) same wireless | USPAT | 2004/02/12 14:59 |
| 7 | 21 | (((integrated adj circuit) ic asic) near2 (includ$3 compris$3 ha$2) near2 (PLD$1 PAL$1 FPLA$1 CPLD$1 EPLD$1 EEPLD$1 LCA$1 FPGA$1)) and actel | USPAT | 2004/02/12 15:13 |
| 8 | 0 | ((((integrated adj circuit) ic asic) near2 (includ$3 compris$3 ha$2) near2 (PLD$1 PAL$1 FPLA$1 CPLD$1 EPLD$1 EEPLD$1 LCA$1 FPGA$1)) and actel) and wireless | USPAT | 2004/02/12 15:13 |
| 9 | 44 | processor near2 pld | USPAT | 2004/02/12 16:23 |
| 10 | 8 | (processor near2 pld) same ic | USPAT | 2004/02/12 16:23 |
| 11 | 640 | wireless near6 ((integrated adj circuit) ic asic) | USPAT | 2004/02/12 16:42 |
| 12 | 18 | ((integrated adj circuit) ic asic) near4 (wireless adj transceiver) | USPAT | 2004/02/12 16:51 |
| 13 | 8 | ("3384832" \| "3836706" \| "5623231" \| "5734298" \| "5963094" \| "6005438" \| "6046641" \| "6094084").PN. | USPAT | 2004/02/12 16:50 |
| 14 | 122 | xilinx and wireless | USPAT | 2004/02/12 16:51 |
| 15 | 23 | xilinx and (wireless adj transceiver) | USPAT | 2004/02/12 16:56 |
| 16 | 7 | xilinx and (wireless adj transceiver) and IC | USPAT | 2004/02/12 17:00 |
| 17 | 1067 | ((integrated adj circuit) IC) near4 transmitter | USPAT | 2004/02/12 17:01 |
| 18 | 7 | (((integrated adj circuit) IC) near4 transmitter) same (PLD$1 PAL$1 FPLA$1 CPLD$1 EPLD$1 EEPLD$1 LCA$1 FPGA$1) | USPAT | 2004/02/12 17:02 |
| - | 17633 | PDC$1 or PDL$1 or "PDC" or "PDCs" or "PDL" or "PDLs" or (program$5 adj logic adj (circuit or device)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/02/12 14:31 |
| - | 17633 | PDC$1 or PDL$1 or "PDC" or "PDCs" or "PDL" or "PDLs" or (program$5 adj logic adj (circuit or device)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2002/11/14 16:55 |
| - | 930585 | eras$3 or program$5 or reprogram$5 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2002/11/14 17:00 |
| - | 9309 | (PDC$1 or PDL$1 or "PDC" or "PDCs" or "PDL" or "PDLs" or (program$5 adj logic adj (circuit or device))) same (eras$3 or program$5 or reprogram$5) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/09/08 15:54 |

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|---|---:|---|---|---|
| – | 0 | (processor or microprocessor) near4 pld near8 (die encas$3) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/09/11 14:59 |
| – | 9 | (processor microprocessor) same pld same (die encas$3) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/09/11 15:00 |
| – | 2 | 6389321.pn. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/02/10 16:21 |
| – | 2 | 5635855.pn. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/02/10 16:22 |
| – | 254966 | (integrated adj circuit) IC | USPAT | 2004/02/10 16:40 |
| – | 1330 | ((integrated adj circuit) IC) near4 processor near4 memory | USPAT | 2004/02/10 16:45 |
| – | 3 | (((integrated adj circuit) IC) near4 processor near4 memory) same pld | USPAT | 2004/02/10 16:41 |
| – | 189 | ((integrated adj circuit) IC) near2 (includ$3 compris$3 ha$4) near2 processor near4 memory | USPAT | 2004/02/10 16:49 |
| – | 61 | 13.clm. | USPAT | 2004/02/10 16:48 |
| – | 8 | 13.ab. | USPAT | 2004/02/10 16:48 |
| – | 42 | (((integrated adj circuit) IC) near2 (includ$3 compris$3 ha$4) near2 processor near4 memory) same $2program$4 | USPAT | 2004/02/11 16:28 |
| – | 6 | ("4656592" \| "5752070" \| "5898677" \| "6152613" \| "6269277" \| "6269467").PN. | USPAT | 2004/02/11 16:09 |
| – | 78 | processor near4 memory near4 (pld plc pla pga) | USPAT | 2004/02/11 16:34 |
| – | 331 | 716/16.ccls. | USPAT | 2004/02/11 16:35 |
| – | 4624 | 716/$.ccls. | USPAT | 2004/02/11 16:35 |
| – | 356 | 716/17.ccls. | USPAT | 2004/02/11 16:40 |
| – | 2313 | $2program$4 near4 (pld (programmable adj logic adj device) plc) | USPAT | 2004/02/11 16:43 |
| – | 146 | ($2program$4 near4 (pld (programmable adj logic adj device) plc)) and 716/$.ccls. | USPAT | 2004/02/11 16:43 |
| – | 54 | ($2program$4 near4 (pld (programmable adj logic adj device) plc)) and 716/17.ccls. | USPAT | 2004/02/11 16:45 |
| – | 46 | (($2program$4 near4 (pld (programmable adj logic adj device) plc)) and 716/17.ccls.) and (integrate$1 near2 circuit) | USPAT | 2004/02/11 17:00 |
| – | 9185 | (integrate$1 near2 circuit) same processor same memory | USPAT | 2004/02/11 16:52 |
| – | 1 | (($2program$4 near4 (pld (programmable adj logic adj device) plc)) and 716/17.ccls.) and ((integrate$1 near2 circuit) same processor same memory) | USPAT | 2004/02/11 16:53 |
| – | 1580 | (single near2 circuit) same processor same memory | USPAT | 2004/02/11 16:53 |
| – | 1 | 6421812.pn. | USPAT | 2004/02/11 17:00 |
| – | 120 | (($2program$4 near4 (pld (programmable adj logic adj device) plc)) and 716/$.ccls.) and (integrate$1 near2 circuit) | USPAT | 2004/02/11 17:17 |

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